

# Reuse Methodology Manual For System-on-a-Chip Designs By Michael Keating; Pierre Bricaud

**By Michael Keating; Pierre Bricaud**

## **Synopsys, Mentor release second edition of RMM | -**

Mentor Graphics Corp. and Synopsys Inc. released a second edition of the Reuse Methodology Manual (RMM), the seminal the reference book on design reuse, at this year

## **Synopsys Press -**

Low Power Methodology Manual Authors: Michael Keating, Reuse Methodology Manual for System-on-a-Chip Designs, Mike Keating and Pierre Bricaud

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## **Michael Keating (Author of Reuse Methodology -**

Michael Keating is the author of Reuse Methodology Manual for System-On-A-Chip Designs (3.75 avg rating, 4 ratings, 0 reviews, published 1998), The Simp

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Reuse Methodology Manual For System-On-A-Chip for creating reusable designs for use in an SoC design methodology. Michael Keating & Pierre Bricaud.

## **Pierre Bricaud (Author of Reuse Methodology Manual -**

Pierre Bricaud is the author of Reuse Methodology Manual (0.0 avg rating, 0 ratings, 0 reviews, published 1999) and Reuse Methodology Manual for System-O

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Michael Keating , Pierre Bricaud, Reuse methodology manual: for system-on-a-chip In this paper we will review how the IP Reuse SoC design methodology has

## **Michael Keating (Author of Reuse Methodology -**

Michael Keating is the author of Reuse Methodology Manual for System-On-A-Chip Designs Michael Keating s Followers.

## **Reuse Methodology Manual - What does RMM stand -**

Reuse Methodology Manual (for System-On-A Methodology Manual (RMM) for System-on-a-Chip Designs established the open, industry standard for design reuse and

## **Michael Keating - B cker - Bokus bokhandel -**

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Genre/Form: Electronic books: Additional Physical Format: Print version: Keating, Michael, 1950 May 2-Reuse methodology manual for system-on-a-chip designs.

### **Diving Into the Reuse Methodology Manual: A -**

Yesterday, I started a series of blog entries that discuss the Reuse Methodology Manual for System-On-A-Chip Designs (the RMM) by Michael Keating and Pierre Bricaud

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Reuse Methodology Manual for System-on-a-Chip Designs, Third Edition outlines a set of best practices for creating reusable designs for use in an SoC design methodology.

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